

DIGITAL DESIGN ACRONYMS			
AHC	Advanced High-speed CMOS	HCT	High-Speed CMOS, TTL compatible
AHCT	Advanced High-speed CMOS TTL compatible	IEEE	Institute of Electrical and Electronic Engineers
ALU	Arithmetic Logic Unit	IOB	I/O Block
AMI	Alternate Mark Inversion	IP	Internet Protocol
ANSI	American National Standards Institute	ISDN	Integrated Services Digital Network
ASCII	American Standard Code for Information Interchange	LCD	Liquid-Crystal Display
ASIC	Application Specific Integrated Circuit	LED	Light-Emitting Diode
ASM	Algorithmic State Machine	LFSR	Linear Feedback Shift Register
ATM	Asynchronous Transfer Mode	LSI	Large Scale Integration
BCD	Binary-Coded Decimal	LVC	Low-Voltage CMOS
BJT	Bipolar Junction Transistor	MCM	Multi-Chip Module
BOM	Bill of Materials	MDS	Mnemonic Documented State
BPRZ	Bipolar Return-to-Zero	MOS	Metal-Oxide Semiconductor
CAD	Computer-Aided Design	MOSFET	Metal-Oxide Semiconductor Field-Effect Transistor
CAE	Computer-Aided Engineering	MSB	Most Significant Bit
CAS	Column Address Strobe	MSI	Medium-Scale Integration
CCD	Charge Coupled Device	MTBF	Mean Time Between Failures
CD	Compact Disc	NMOS	<i>n</i> -channel MOS
CLB	Configurable Logic Block	NRE	Nonrecurring Engineering
CML	Current-Mode Logic	NRZ	Non-Return-to-Zero
CMOS	Complementary MOS	OE	Output Enable
CPLD	Complex PLD	OTP-ROM	One-Time Programmable ROM
CPU	Central Processing Unit	PCB	Printed-Circuit Board
CRC	Cyclic-Redundancy Check	PECL	Positive ECL
CS	Chip-Select	PLA	Programmable Logic Array
DDR-DRAM	Double-Data-Rate synchronous DRAM	PLD	Programmable Logic Device
DFT	Design for Testability	PMOS	<i>p</i> -channel MOS
DIP	Dual In-line Pin	PROM	Programmable Read-Only Memory
DPLL	Digital Phase-Locked Loop	PWB	Printed Writing Board
DRAM	Dynamic RAM	RAID	Redundant Array of Inexpensive Disks
DSL	Digital Subscriber Line	RDRAM	Rambus DRAM
DVD	Digital Versatile Disc	ROM	Read-Only Memory
ECL	Emitter-Coupled Logic	RWM	Read/Write Memory
EDO-DRAM	Extended-Data-Out DRAM	SCR	Silicon-Controlled Rectifier
EEPROM	Electrically Erasable Programmable Read-Only Memory	SDRAM	Synchronous DRAM
ESDRAM	Enhanced Synchronous DRAM	SSI	Small Scale Integration
FCC	Federal Communications Commission	TTL	Transistor-Transistor Logic
FCT	Fast CMOS, TTL compatible	VHDL	VHSIC Hardware Description Language
FIFO	First-In, First-Out Memory	VHSIC	Very High Speed Integrated Circuit
FPGA	Field-Programmable Gate Array	VLSI	Very Large Scale Integration
HC	High-Speed CMOS	WE	Write-Enable